

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Yoshitsugu MORITA et al. Confirmation No.: 6909  
Serial No.: 10/584,032  
Filed: June 22, 2006  
Group Art Unit: 2812  
Examiner: Patel, Reema  
For: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THEREOF

**AMENDMENT**

Mailstop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

In response to the Office Action communication from the Examiner dated March 28, 2008, please amend the claims as follows and consider the accompanying remarks. A complete listing of all claims pending in the application is included in accordance with 37 C.F.R. § 1.121. This response is timely filed; thus, it is believed that no fees are presently due.